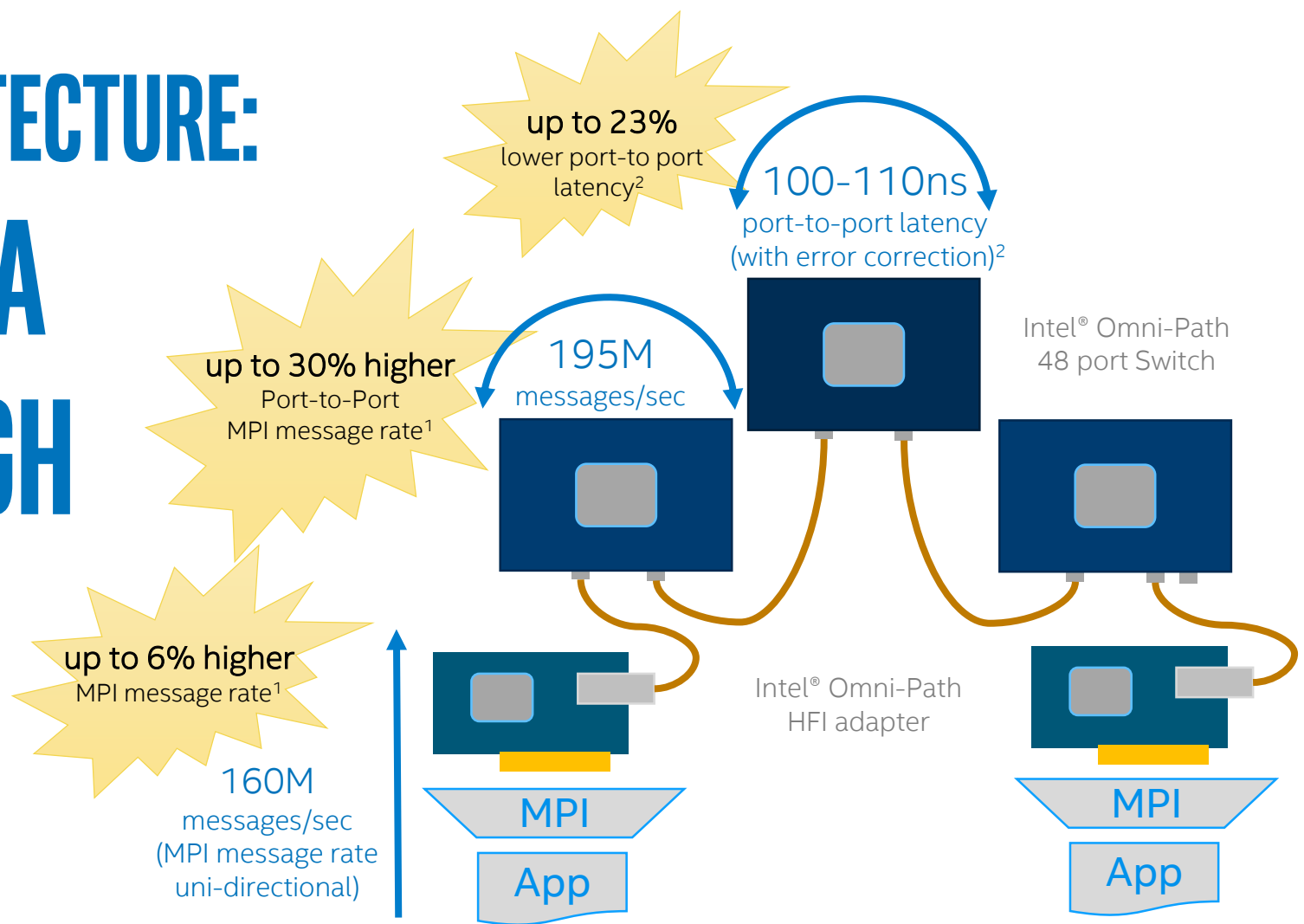


# INTEL® OMNI-PATH ARCHITECTURE: ACCELERATING DATA MOVEMENT THROUGH THE FABRIC



<sup>1</sup> Based on Intel projections for Wolf River and Prairie River maximum messaging rates, compared to Mellanox CS7500 Director Switch and Mellanox ConnectX-4 adapter and Mellanox SB7700/SB7790 Edge switch product briefs posted on [www.mellanox.com](http://www.mellanox.com) as of July 1, 2015.

<sup>2</sup> Latency reductions based on Mellanox CS7500 Director Switch and Mellanox SB7700/SB7790 Edge switch product briefs posted on [www.mellanox.com](http://www.mellanox.com) as of July 1, 2015, compared to Intel measured data that was calculated from difference between back to back osu\_latency test and osu\_latency test through one switch hop. 10ns variation due to "near" and "far" ports on an Intel® OPA edge switch. All tests performed using Intel® Xeon® E5-2697v3 with Turbo Mode enabled.

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